

Prospects of M.Tech
in
Applied Electronics and Instrumentation Engg.
at the AEIE Dept. of
Heritage Institute of Technology

50% tuition fee waiver for all eligible candidates
All GATE qualifiers will get Scholarships at per
MHRD

Eligibility criteria for admission:

- All students from engineering fields i.e. AEIE, ICE, EE, EEE, ECE
- All students from science fields i.e. M.Sc. Electronics Science and M.Sc. in Instrumentation Science

Facilities at the department:

- Industry funded (*KernelSphere technologies Pvt. Ltd*) **Industrial Internet of Things Lab (Centre of Excellence)**
- State of the art **Embedded Systems Lab** with facility for development on latest generation ARM and AVR micro-controllers
- **Micro Sensor fabrication/visualization** facility aided by **IntelliSense Corp., USA**
- State of the art **DSP Lab** with modern signal processing tools
- State of the art **Industrial Instrumentation Lab**
- State of the art **Process Control Lab** with fully upgraded process instruments

Industry Academia Collaborations:

- **MoU with Semiconductor Laboratory, ISRO, Govt. of India, Chandigarh**, for PG students to pursue research in the field of micro sensor fabrication & characterization
- **MoU with KernelSphere technologies Pvt. Ltd.** (Indian Multinational)
- **Collaboration with IntelliSense Corp., USA**, for micro sensor design, fabrication and testing purpose
- **Collaboration with RRCAT, Indore**, for fabrication of micro-fluidic devices and **CGCRI, Kolkata**, for fabrication and characterization of MEMS devices

Future Opportunities for M.Tech AEIE Students:

- Research opportunities in Premier Academic Institutes including Ph.D. programmes, SRF, research Scholars, etc.
- Faculty/Research position in various academic/R&D institutions
- Job in both IT and core sector through campus interview

Some milestones by previous M.Tech Students:

- In total 16 of our students (MAKAUT and Autonomy) received medals for their outstanding performance in academics
- Two BIRAC-SRISTI Appreciation Awards of 1 L each in year 2017
- Awards for Best project & Best stall in Industry-Academia Conclave on 27-29th March, 2017 at Milan Mela, organized by MAKAUT
- Recipient of the prestigious Gandhian Young Technological Innovation Award 2016 at Rastrsapati Bhawan, New Delhi
- Bagged best research paper award at international conference like CODEC 2012 and MICRO 2016
- Best PG thesis award from Institute of Smart Structures & Systems (a body of IISc., Bangalore and DRDO) in the year 2015
- AEIE M.Tech Students working in: TCS, Infosys, Cognizant, Capgemini, IFRAC, PCBL, NICCO Projects, HoneyWell, Yokogawa, Siemens, Exide, AAI, Indian Railways and so many.

Note from the HoD of AEIE Dept.:

The post graduate program in AEIE started in the year **2006** and today it is one of the best academic centres to carryout master's degree on AEIE in Eastern India as it is the only one PG course in AEIE got accreditation by NBA. In this 13 year journey, the AEIE dept. has always been dynamic at work to keep the curriculum at par with the present industry standards and as a result the passes out students are placed in reputed organisations. We have some of the best faculty members teaching the PG course, who have obtained their Ph.D. degrees from premier institutes of India like IISc., IITs, JU, CU, etc. The department has been privileged enough to be awarded with some of the best in class facilities. The faculty research spans in vast domains like Micro sensors, Embedded Systems, Signal processing, Machine Learning, AI, Intelligent Control systems, IoT and may more cutting edge technologies. We feel proud to see our PG alumnus getting exposure to work at RRCAT, Indore; IISc., Bangalore; CISO, Chandigarh and SCL, ISRO.

-----Prof. (Dr.) Madhurima Chattopadhyay,
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B.Tech.(JU),M.E.(CU),Ph.D.(IISc,Bangalore)

For Further Query, please contact:

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