

ECE Dept. M.Tech VLSI Prospect in Heritage Institute of Technology:

VLSI Methodology:

- Complete VLSI Chip Design Method: 180nm, 90nm, **45nm** Technology
- Circuit Research using Latest Technology: 45nm, 32nm, 22nm, **14nm and Below**
- Research on Device and Process using Latest Nano Technology: SOI, **FINFET**, CNT, **Solar Cell**

VLSI CAD Tools:

- Industry Standard **Cadence** Frontend & Backend Design: **Analog and Digital** Flow
- Industry Standard **Mentor Graphics** Backend Design
- Industry Standard **TCAD Synopsys** Simulator for Nano Technology Research
- FPGA Design: **XILINX** Vivado CAD Tool/FPGA & Embedded System Kits
- Analog System Design: **Texas Instruments** Analog Design Kits

VLSI Industry Academia Collaborations and Projects:

- **India Chip Program** (Make in India Initiative) in Collaboration with **NETRA** Industry Academia Platform under Umbrella of **IESA** and **ESSC, GOI**
- **Sankalp Semiconductor** (Indian Multinational)
- **Texas Instrument** University Program (**Cranes Software**)
- **UMC** (United Microelectronics, Multinational) and **IMEC** (European Research Institute) through **EUROPRACTICE IC Service**
- Solar Cell Projects in Collaboration with **IIEST**, Shibpur

Future Opportunity for ECE Dept. M.Tech VLSI Students:

- Research in Premier Academic Institutes including PhD
- Faculty Position in various Academic Institutes
- Job in IT Sector (On Campus)
 - ❖ ECE M.Tech VLSI Students working in: TCS, Infosys, Cognizant, Capgemini
- Jobs in Core Sector (Opportunistic Off Campus)
 - ❖ ECE M.Tech VLSI Students working in: Intel, Sankalp Semiconductor, Interra, SmartPlay, Tata Elxsi

Note From ECE Dept. M.Tech VLSI Coordinator:

M.Tech VLSI Program in ECE started from 2011. Today it is one of best VLSI Center in Eastern India. For last few years our key focus has been to prepare Students for Industry and Research. We have some of best faculty Members who spent decades in USA. Many faculty members are involved in PhD and Research in collaboration with premier Institutes like Jadavpur University, IIEST. Today we have outstanding Infrastructure and several prestigious Industry Collaborations and projects. We frequently conduct VLSI Workshops. We feel proud that in last 3 years, 6 ECE M.Tech VLSI Students got offer in Intel, B'lore.

For Further Query, please contact:

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