HERITAGE INSTITUTE OF TECHNOLOGY, KOLKATA

Notice for Odd Semester Examinations 2023 for 1st Year Students

Date: 22.12.2023

Notice No. HRIT/0914/3932

It is for the information of all **B.Tech (1st Sem), M.Tech (1st Sem) and MCA (1st Sem)** students (Regular & Backlog) that Odd Semester Theory Examinations, 2023 will commence on and from **22**nd **January, 2024.** Time table is attached with this notice.

Students are hereby informed that Odd Semester Examinations, 2023 form will be available on the Institute website (www.heritageit.edu/ExamCell.aspx) on and from 26.12.2023 (Tuesday) from 4:00 PM onwards. Students are advised to find their autonomy roll number from the following link

https://www.theheritage.ac.in/knowyourautonomyrollno.aspx

Student should fill up the Examination form online entering their Autonomy Roll Nos. and uploading their recent colour passport size photograph (*.jpg format only) at the appropriate place. The size of the image would be 160px x 170px (i.e 5KB). After saving the form, students are required to pay the prescribed fees of Rs.1200/- for regular papers (non-refundable) and Rs.1000/- for backlog papers (non-refundable) strictly through online facility (net banking / debit card / credit card) only.

Last date for online filling of forms and submission of fees is **09.01.2024** (up to 4:00 PM).

Students are also directed to note the following points:

- That the acceptance of form is only provisional and that if any student fails to secure minimum attendance, he/she is liable to be disallowed to appear for Odd Semester Examinations 2023 and, in such case, his/her Examination Fees would be refunded.
- Admit Card will be generated one week before the commencement of Examinations.
- Students are instructed not to change / discontinue their mobile numbers mentioned in the application form.
- Students are instructed to use Mozilla Firefox browser for filling up online Examination form.
- All defaulters are informed that unless they clear their outstanding dues they will not be able to fill up their online examination form.
- For any difficulties related to technical issues student should intimate to Mr.Suman Chatterjee in the System Department (Room No. CME 009).

Prof. N. P. Nayak

(Controller of Examinations)



OFFICE OF THE CONTROLLER OF EXAMINATIONS HERITAGE INSTITUTE OF TECHNOLOGY

TIME TABLE FOR ODD SEMESTER B.TECH, M.TECH AND MCA 1st SEMESTER THEORY EXAMINATIONS, 2023

DATE: 22.01.2024 (Monday)

TIME: 10.00 AM - 12.30 PM

PAPER CODE	PAPER NAME	DEPARTMENT
MTH 1101	Mathematics – I (Regular)	AEIE, AI&ML, BT, CE, CHE,
		CSBS, CSE, Data Sc. ECE, EE,
	· ·	IoT&CS, IT and ME
MATH 1101	Mathematics – I (Backlog*)	AEIE, AI&ML, BT, CE, CHE,
		CSBS, CSE, Data Sc. ECE, EE,
		IoT&CS, IT and ME
MCA 1101	Object Oriented Programming with Java (Regular)	MCA
MCAP 1101	Object Oriented Programming with Java (Backlog**)	MCA
AEIE 5101	Advanced Digital Signals & Systems (Regular & Backlog*)	AEIE
BIOT 5101	Advanced Genetic Engineering (Regular & Backlog*)	BT
CSEN 5101	Advanced Data Structures (Regular & Backlog*)	CSE
ECEN 5101	Antenna & Radiating Systems (Regular & Backlog*)	ECE
REEN 5101	Renewable Energy Resource and Characteristics	RE ,
VLSI 5101	Digital VLSI IC Design (Regular & Backlog*)	VLSI

DATE: 24.01.2024 (Wednesday)

TIME: 10.00 AM - 12.30 PM

PAPER CODE	PAPER NAME	DEPARTMENT
HUM 1001	English for Technical Writing (Regular)	AEIE, BT, CE, CHE, ECE, EE, and ME
HUM 1002	Universal Human Values and Professional Ethics (Regular)	AI&ML, CSBS, CSE, Data Sc., IoT&CS, and IT
ELEC 1001	Basic Electrical Engineering (Backlog*)	AEIE, CSBS, CSE, ECE & IT
CSEN 1001	Programming for Problem Solving (Backlog*)	AI&ML, BT, CE, CHE, Data Sc. EE, IoT&CS and ME
MCA 1102	Programming with Python (Regular)	MCA
MCAP 1102	Programming with Python (Backlog**)	MCA
AEIE 5102	Programming Language for Embedded IoT Systems (Regular & Backlog*)	AEIE
BIOT 5102	Physicochemical Techniques in Biotechnology (Regular & Backlog*)	ВТ
MATH 5101	Advanced Discrete Mathematics & Statistical Methods (Regular & Backlog*)	CSE
ECEN 5102	Wireless & Mobile Communication (Regular & Backlog*)	ECE
REEN 5102	Non-Solar Renewable Energy	RE
VLSI 5102	Embedded Systems Design (Regular & Backlog*)	VLSI

Controller of Examinations
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OFFICE OF THE CONTROLLER OF EXAMINATIONS HERITAGE INSTITUTE OF TECHNOLOGY

TIME TABLE FOR ODD SEMESTER B.TECH, M.TECH AND MCA 1^{st} SEMESTER THEORY EXAMINATIONS, 2023 TIME: 10.00 AM - 12.30 PM

DATE: 29.01.2024 (Monday)

PAPER CODE	PAPER NAME	DEPARTMENT
CHM 1001	Chemistry – I (Regular)	AEIE, BT, CE, CHE, ECE, EE, and ME
PHY 1001	Physics – I (Regular)	AI&ML, CSBS, CSE, Data Sc., IoT&CS, and IT
CHEM 1001	Chemistry – I (Backlog*)	AEIE, CSBS, CSE, ECE & IT
PHYS 1001	Physics – I (Backlog*)	AI&ML, BT, CE, CHE, Data Sc. EE, IoT&CS and ME
MCA 1103	Artificial Intelligence (Regular)	MCA
MCAP 1103	Artificial Intelligence (Backlog**)	MCA
AEIE 5103	Research Methodology & IPR (Regular & Backlog*)	AEIE
BIOT 5103	Research Methodology, Bioethics & IPR (Regular & Backlog*)	ВТ
CSEN 5102	Research Methodology & IPR (Regular & Backlog*)	CSE
ECEN 5103	Research Methodology & IPR (Regular & Backlog*)	ECE & VLSI
REEN 5103	Research Methodology and IPR	RE

DATE: 31.01.2024 (Wednesday)

TIME: 10.00 AM - 12.30 PM

PAPER CODE	PAPER NAME	DEPARTMENT
ELE 1001	Basic Electrical Engineering (Regular)	AEIE, BT, CE, CHE, ECE, EE, and ME
ECE 1001	Introduction to Electronics Devices & Circuits (Regular)	AI&ML, CSBS, CSE, Data Sc., IoT&CS, and IT
HMTS 1011	Communication for Professionals (Regular & Backlog*)	AEIE, CSBS, CSE, ECE, IoT&CS and IT
ECEN 1011	Basic Electronics (Regular & Backlog*)	Al&ML, BT, CE, CHE, Data Sc. EE and ME
MCA 1104	Mathematical Foundations (Regular)	MCA
MATH 1102	Mathematical Foundations (Backlog**)	MCA
AEIE 5133	Instrumentation and Industrial Automation (Regular & Backlog*)	AEIE
BIOT 5131	Advanced Enzyme Technology (Regular & Backlog*)	ВТ
CSEN 5131	Machine Learning (Regular & Backlog*)	CSE
ECEN 5131	Wireless Ad Hoc and Sensor Networks (Regular & Backlog*)	ECE
REEN 5141	Material for Renewable Energy Application	RE
VLSI 5132	VLSI IC Fabrication (Regular & Backlog*)	VLSI



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TIME TABLE FOR ODD SEMESTER B.TECH, M.TECH AND MCA 1st SEMESTER THEORY EXAMINATIONS, 2023 DATE: 02.02.2024 (Friday) TIME: 10.00 AM - 12.30 PM

PAPER CODE	PAPER NAME	DEPARTMENT
CSE 1001	Programming for Problem Solving (Regular)	AEIE, BT, CE, CHE,
		ECE, EE, and ME
MCA 1105	Information System Analysis and Design (Regular)	MCA
HMTS 1102	Oral and Written Communication (Regular & Backlog**)	MCA
AEIE 5141	Mechatronics (Regular & Backlog*)	AEIE
BIOT 5141/	Agricultural Biotechnology / Advanced Environmental	BT
5142	Biotechnology (Regular & Backlog*)	
CSEN 5141	Data Science (Regular & Backlog*)	CSE
ECEN 5141	Satellite Communication Applications (Regular & Backlog*)	ECE
VLSI 5142	Modelling of VLSI Device (Regular & Backlog*)	VLSI
REEN 5144	Thermal and Electrical Energy Fundamentals (Regular &	RE
	Backlog*)	

DATE: 05.02.2024 (Monday)

TIME: 10.00 AM - 12.30 PM

PAPER CODE	PAPER NAME	DEPARTMENT
DIMA 5116	Disaster Management (Regular & Backlog*)	AEIE, BT, CSE,
		ECE, VLSI & RE

* 2018 (2018, 2019, 2020, 2021 & 2022) onwards batches of students

** 2020 (2020, 2021 & 2022) onwards batches of students

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