



OFFICE OF THE CONTROLLER OF EXAMINATIONS
HERITAGE INSTITUTE OF TECHNOLOGY

TIME TABLE FOR Separate Supplementary Examination B.TECH (1st, 2nd, 7th & 8th), M.Tech (1st & 2nd) AND
MCA (1st, 2nd) SEMESTER THEORY EXAMINATIONS, 2023
(UNDER AUTONOMY)

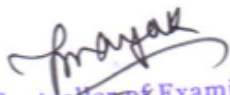
DATE: 16.08.2023 (Wednesday)		TIME: 10 AM – 01 PM
PAPER CODE	PAPER NAME	DEPARTMENT
CSEN 4132	Cryptography & Network Security (Backlog*)	CSE

DATE: 16.08.2023 (Wednesday)		TIME: 02.00 PM – 05.00 PM
PAPER CODE	PAPER NAME	DEPARTMENT
CSEN 4233	Pattern Recognition (Backlog*)	CSE
MECH 4243	Gas Dynamics and Jet Propulsion (Backlog*)	ME
ECEN 4241	Remote Sensing using Satellites (Backlog**)	ECE

DATE: 17.08.2023 (Thursday)		TIME: 10.00 AM – 01.00 PM
PAPER CODE	PAPER NAME	DEPARTMENT
MATH 1101	Mathematics – I (Regular / Backlog* & Backlog**)	All B.Tech
MCAP 1101	Object Oriented Programming with Java (Regular & Backlog***)	MCA
BIOT 5101	Advanced Genetic Engineering (Regular & Backlog*)	BT
CSEN 5101	Advanced Data Structures (Regular & Backlog*)	CSE
VLSI 5101	Digital VLSI IC Design (Regular & Backlog*)	VLSI

DATE: 17.08.2023 (Thursday)		TIME: 02.00 PM – 05.00 PM
PAPER CODE	PAPER NAME	DEPARTMENT
MATH 1201	Mathematics II (Regular & Backlog* and Backlog**)	All B.Tech
MCAP 1201	Data Structures and Algorithms (Regular & Backlog***)	MCA
CSEN 5202	Soft Computing (Regular & Backlog*)	CSE
VLSI 5201	Analog VLSI IC Design (Regular & Backlog*)	VLSI

DATE: DATE: 18.08.2023 (Friday)		TIME: 10 AM – 01 PM
PAPER CODE	PAPER NAME	DEPARTMENT
ECEN 4121	Software Defined Radio (Backlog*)	CSE
AEIE 4126	Optical Instrumentation	CHE
INFO 4122	Machine Learning (Backlog*)	IT


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DATE: 21.08.2023 (Monday)		TIME: 10.00 AM – 01.00 PM
PAPER CODE	PAPER NAME	DEPARTMENT
PHYS 1001	Physics – I (Regular and Backlog* & Backlog**)	All B.Tech
MATH 5101	Advanced Discrete Mathematics & Statistical Methods (Regular & Backlog*)	CSE
VLSI 5132	VLSI IC Fabrication (Regular & Backlog*)	VLSI

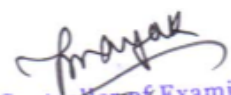
DATE: 21.08.2023 (Monday)		TIME: 02.00 PM – 05.00 PM
PAPER CODE	PAPER NAME	DEPARTMENT
CHEM 1001	CHEMISTRY I (Regular & Backlog* and Backlog**)	All B.Tech
MCAP 1202	COMPUTER COMMUNICATION NETWORKS (Regular & Backlog***)	MCA
CSEN 5234	THEORY OF COMPUTATION	CSE
VLSI 5202	VLSI DESIGN, TESTING AND VERIFICATION	VLSI

DATE: 22.08.2023 (Tuesday)		TIME: 10.00 AM – 01.00 PM
PAPER CODE	PAPER NAME	DEPARTMENT
CSEN 4111	Compiler Design	CSE
ECEN 4111	Micro Electronics and Analog VLSI Design	ECE

DATE: 23.08.2023 (Wednesday)		TIME: 10.00 AM – 01.00 PM
PAPER CODE	PAPER NAME	DEPARTMENT
ELEC 1001	Basic Electrical Engineering (Regular & Backlog*)	All B.Tech
MECH 1101	Engineering Mechanics (Backlog**)	All B.Tech
CSEN 5131	Machine Learning (Regular & Backlog*)	CSE
VLSI 5142	Modelling of VLSI Device (Regular & Backlog*)	VLSI

DATE: 23.08.2023 (Wednesday)		TIME: 02.00 PM – 05.00 PM
PAPER CODE	PAPER NAME	DEPARTMENT
CSEN 1001	Programming for Problem Solving (Regular & Backlog*)	All B.Tech
MECH 1201	ENGINEERING THERMODYNAMICS & FLUID MECHANICS (Backlog**)	All B.Tech
MCAP 1203	SOFT COMPUTING (Regular & Backlog***)	MCA
CSEN 5243	CLOUD COMPUTING	CSE
VLSI 5242	ADVANCED NANO DEVICES	VLSI

DATE: 25.08.2023 (Friday)		TIME: 10.00 AM – 01.00 PM
PAPER CODE	PAPER NAME	DEPARTMENT
HMTS 1011	Communication for Professionals (Regular & Backlog*)	All B.Tech
DIMA 5116	Disaster Management (Regular & Backlog*)	All M.Tech


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DATE: 25.08.2023 (Friday)		TIME: 02.00 PM – 05.00 PM
PAPER CODE	PAPER NAME	DEPARTMENT
HMTS 1202	BUSINESS ENGLISH (Regular & Backlog*)	All B.Tech
HMTS 1101	Business English (Backlog**)	All B.Tech
MCAP 1204	OPERATING SYSTEMS (Regular & Backlog***)	MCA
VLSI 5232	LOW POWER VLSI DESIGN	VLSI

DATE: 28.08.2023 (Monday)		TIME: 10.00 AM – 01.00 PM
PAPER CODE	PAPER NAME	DEPARTMENT
ECEN 1011	BASIC ELECTRONICS (Backlog*)	All B.Tech
ECEN 1001	BASIC ELECTRONICS ENGINEERING (Backlog**)	All B.Tech
MCAP 1205	INFORMATION SYSTEM ANALYSIS AND DESIGN (Regular & Backlog***)	MCA

* 2018 (2018, 2019, 2020 & 2021) onwards batches of students

** Before 2018 (2014, 2015, 2016 & 2017) batches of students

*** 2020 (2020 & 2021) onwards batches of students

Prayak
 Controller of Examinations
 Heritage Institute of Technology