



M.Tech VLSI Prospect in Heritage Institute of Technology (ECE Dept)

CAD Tool Infrastructure in VLSI Laboratory:

- Industry Standard **Cadence** Frontend & Backend Design: **Analog and Digital** Flow
- Industry Standard **Mentor Graphics** Backend Design
- Industry Standard **TCAD Synopsys** Simulator for Nano Technology Research
- FPGA Design: **XILINX** Vivado CAD Tool/FPGA & Embedded System Kits
- Analog System Design: **Texas Instruments** Analog Design Kits

VLSI Research Facility:

- Complete VLSI Chip Design Method: 180nm, 90nm, **45nm** Technology
- Circuit Research using Latest Technology: 45nm, 32nm, 22nm, **14nm** and **Below**
- Research on Latest Nano Technology: SOI, **FINFET**, CNT, **Solar Cell**

VLSI Industry Academia Collaborations and Projects:

- **DXCorr Design** (Indian Multinational)
- **Sankalp Semiconductor** (Indian Multinational)
- **e-COE** (Electronics Center of Excellence), supported by **IESA** (India Electronics and Semiconductor Association) and **ESSCI, Skill India**, Govt of India
- **Micro-Chip** Design and manufacturing through **India Chip Program** (Make in India Initiative) in collaboration with **e-COE** (Electronics Center of Excellence)
- **Texas Instrument** University Program (**Cranes Software**)
- Foundry access of **UMC** (United Microelectronics, Multinational) through **IMEC** (European Research Institute) by **EUROPRACTICE IC Service**
- Collaboration with **IIEST**, Shibpur for Solar Cell Projects

Future Opportunity for M.Tech VLSI Students:

- Research and PhD in Premier Institutes & Organizations (**IIT, IIEST, NIT, COE, JU**)
- Faculty Position in various Academic Institutes
- Job in IT Sector (On and Off Campus): **TCS, Infosys, Cognizant, Capgemini**
- Jobs in Core Sector (On and Off Campus): **Intel, DXCorr Design, Sankalp Semi, ST Microelectronics, Interra, SmartPlay, Synopsys, TSMC, Adeptchips**

Message From M.Tech VLSI Coordinator:

M.Tech VLSI Program in ECE started from 2011. Today it is one of **best VLSI Center in Eastern India**. We have some of best faculty Members who spent decades in USA. Today we have outstanding Infrastructure and several prestigious Industry Collaborations and projects. In last **6 years, 7 M.Tech VLSI Students got offer at Intel, B'lore.**

For Further Query, please contact:

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