

HERITAGE INSTITUTE OF TECHNOLOGY

TIME TABLE

<ECE>

M. Tech 2nd Year 2nd Semester

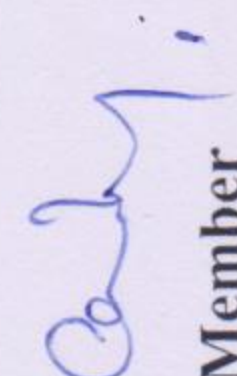
VLSI

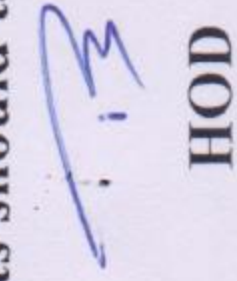
SESSION: 2024-2025

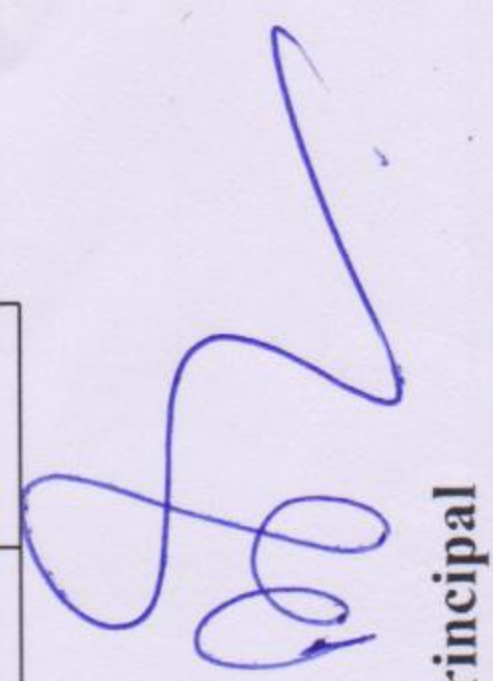
DAY	GROUP	9.00 AM – 10.00 AM	10.00 AM – 11.00 AM	11.00 AM – 12 NOON	12 NOON – 1.00 PM	1.00 PM – 2.00 PM	2.00 PM – 3.00 PM	3.00 PM – 4.00 PM	4.00 PM – 5.00 PM	5.00 PM – 6.00 PM
MON	GR. 1	PROJECT/ VLSI 6295/ ICT 602								
	GR. 2									
TUE	GR. 1	PROJECT/ VLSI 6295/ ICT 602			Mentoring/ ICT 614		Mentoring/ ICT 614			
	GR. 2									
WED	GR. 1	PROJECT/ VLSI 6295/ ICT 602								
	GR. 2									
THU	GR. 1	Mentoring/ ICT 614			BREAK		PROJECT/ VLSI 6295/ ICT 602		PROJECT/ VLSI 6295/ ICT 602	
	GR. 2									
FRI	GR. 1	PROJECT/ EVLSI 6295/ ICT 602								
	GR. 2									

To be Effective from 15.01.2025

A minimum of 75% attendance is mandatory for being eligible to sit for the End-Semester Examination
Students should target 100% attendance


Member


HOD


Principal

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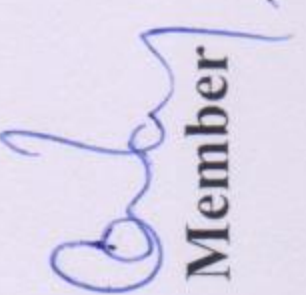
VLSI

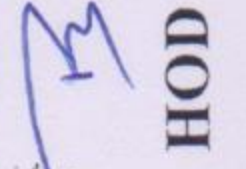
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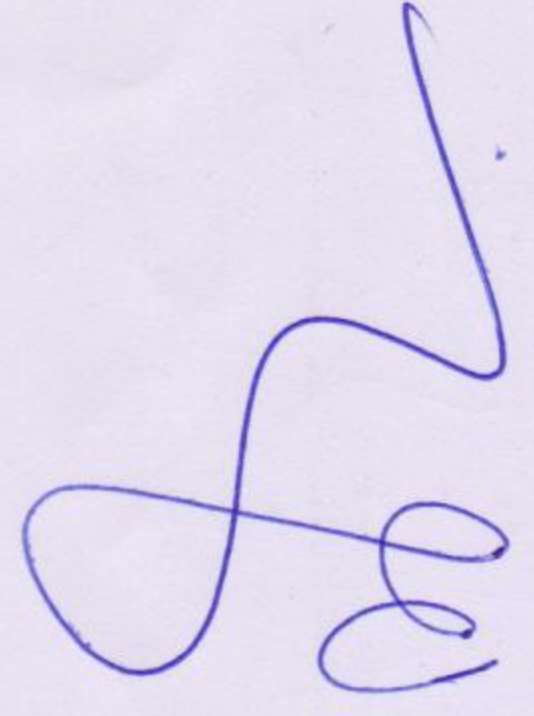
2nd Year, Semester II

Sl. No.	Course Type	Code	Course Title	Contact Hours/Week			Credits
				L	T	P	
1	Dissertation	VLSI6295	Dissertation Phase-II	0	0	32	14
2	Grand Viva	VLSI6297	Comprehensive Viva Voce	-	-	-	2
Total of Semester				0	0	32	16

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