

Memorandum of Understanding
Between



Electronics Center of Excellence (e-COE)

And



Heritage Institute of Technology,
Kolkata

Memorandum of Understanding

About Electronics Center of Excellence (e-COE):

e-COE (Electronics Center of Excellence) has been established by Electronics Sector Skills Council of India (ESSCI), GOI, under the Skill India program of Govt. of India under mentorship of India Electronics & Semiconductor Association (IESA). The center is located at DCB-621, DLF Cyber City, Patia, Bhubaneswar.

Electronic Center of Excellence (e-COE) is a localized hub with three primary focus areas –

- 1) Industry oriented Training for Electronics System Design & Manufacturing (ESDM)
- 2) Industry-Academia Research for productization
- 3) Incubations with Technology solutions for social impact

As one of the primary responsibility, e-COE provides industry oriented semiconductor electronics courses in VLSI Domain. Pedagogy of these courses are based on direct guidance of ESDM Industry experts. Each training program also have a counselling, interaction and mock interview by ESDM industry experts and industry partners.

At the end of the course the students are certified by e-COE. In addition to this, ESSCI conducts an assessment test based on which ESSCI certification is provided to qualified students. So by the end of course, students get a Govt. of India certification in VLSI training. e-COE provides Placement Assistance to the trained students, e-COE helps students to get connected to Internship Opportunities at the end of the courses.

About Heritage Institute of Technology, Kolkata (HITK):

Heritage Institute of Technology, 994, Madurdaha, Chowbaga Road, Anandapur, P.O. East Kolkata Township, Kolkata – 700107, West Bengal, India, is a NBA, NAAC accredited leading Private Engineering college as established in 2001 and affiliated to Maulana Abul Kalam Azad University of Technology, West Bengal (formerly known as West Bengal University of Technology). This institute gained 'Autonomous Status' in 2014. Heritage Institute of Technology is a leading College offering (a) Undergraduate and Postgraduate courses in (i) Engineering and Technology and (ii) Applied Sciences, leading to B.Tech, M.Tech and MCA Degrees in different disciplines.

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Electronics Center of Excellence (**e-COE**) hereinafter will be called and referred to as "**e-COE**" (which term of expression shall, unless excluded by or repugnant to the context or subject, be deemed to mean and include its successors-in-office, administrators, legal representatives and/or assigns) on the ONE PART

and

Heritage Institute of Technology, Kolkata (**HITK**) hereinafter will be called and referred to as "**HITK**" (which term of expression shall, unless excluded by or repugnant to the context or subject, be deemed to mean and include its successors-in-office, administrators, legal representatives and/or assigns) on the OTHER PART.

1. Purpose of the Agreement:

e-COE and **HITK** have mutually agreed to work together for:

- 1) Industry oriented **Training** for Electronics System Design & Manufacturing (**ESDM**)
- 2) Industry-Academia **Research** for productization

e-COE and **HITK** have mutually agreed to work together for training of **HITK** students on VLSI and its related domain at the **e-COE** Center to make them employable in core Semiconductor Industry. **e-COE** also works closely with its industry partner in semiconductor area and gets them connected to the students for possible scope of **internship** and **job** in respective organizations.

Another priority is to endeavor into state-of-the-art collaborative **Research** in the field of Electronic System Design and Manufacturing (**ESDM**) and Internet of Things (**IoT**).

e-COE and **HITK** envision this partnership to excel the growth of **ESDM** manpower development and research mindset in Electronics promoting the initiative of **Make-in-India**. This agreement describes their understandings and commitments to this effort.

2. Scope and Duration:

e-COE and **HITK** will work together to bring & train the students into the domain of VLSI for various employment oriented programs. They will also engage into collaborative Research in VLSI Domain.

Initial duration of this agreement is from **September 25, 2018** to **September 24, 2020**. The same can be renewed in future on mutually agreed terms & conditions.

The aforementioned agreement shall specifically be focused and will execute in the following way:

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